

Active power filter with hysteresis current control loop using rectifier boost technique

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ABSTRACT

This paper presents the Hysteresis Current Control (HCC) to improve the power quality of power electronic converters. The development of HCC was implemented using Active Power Filter (APF) function based on rectifier boost technique to control the range of upper and lower bands. Through this technique, the supply current waveform followed the shape of the sinusoidal reference signal, thus, the distorted input current waveform becomes sinusoidal and in the same phase with the input voltage. As a result, the THD level and switching losses can be reduced, thus improving the power factor of the power supply system. In order to verify the proposed operation, validation of the proposed HCC was done through MATLAB. Selected simulation results are presented

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1. INTRODUCTION

In the era of growing technology, many industries utilize power electronic applications, such as uninterruptible power supply (UPS), switch mode power supply (SMPS) and many more [1-3]. The use of power electronic converters could lead to non-sinusoidal input current which contains harmonics that are harmful to electrical devices. This, in turn, increases power quality issues, such as continuous distortion of normal voltage [4] in the power supply system.

Different methods have been suggested to solve this harmonics problem [5-7]. Among the known methods is using passive filters that are fed to the non-linear loads (NLL). This result in improvements in power factor and harmonic suppression. It also exhibits lower impedance at a tuned harmonics frequency. This method is popular due its simplicity, reliability, efficiency and low cost [8-9]. However, this method lack in current compensation, tuning and parallel resonant problems with random frequency variations.

Therefore, APF is introduced to rectify this issue. APF compensates the non-sinusoidal input current waveform of the NLL. This method solves the harmonic problem but introduces significant switching losses and reduced efficiency [10]. As a result, current control techniques such as Pulse Width Modulation technique, Delta Modulation and Hysteresis Current Control (HCC) are introduced to overcome the switching losses [11-13]. In this paper, HCC is proposed as the foundation to be implemented in the APF.

2. PROPOSED HYSTERESIS CURRENT CONTROL

The HCC method has been recognized as the best approach for APF function, electrical drives and power semiconductor converters [14-19]. HCCs have a variable switching frequency under different load conditions and/or command current. The features of such technique are their capability to stabilized non-linear load condition with a good transient response and high accuracy. Other than that, through this technique, the peak to peak value of current ripple can be controlled according to the desired hysteresis upper and lower band limits [20-24].

In this paper, HCC was implemented in the closed current control loop, as shown in Figure 1. Due to non-characteristic harmonics in the APF, HCC can control the peak-to-peak value of the ripple current in desired hysteresis band limit. To control the switching losses in the supply current, HCC was injected into the current loop of the APF by replacing the repeating sequences and the relational operators with hysteresis band limits. The hysteresis band limits control the supply current's upper band and lower bands. The supply current initially passes through the subtractor which serves as a converter which uses different overflow modes to display the output data form [25]. Next, the signal goes through the PI controller to remove the occurring error signal [26]. The main objective of HCC is to make sure the supply current follows the reference current signal. To verify the current ripple to follow the signal, step response controller is installed in the proposed design.

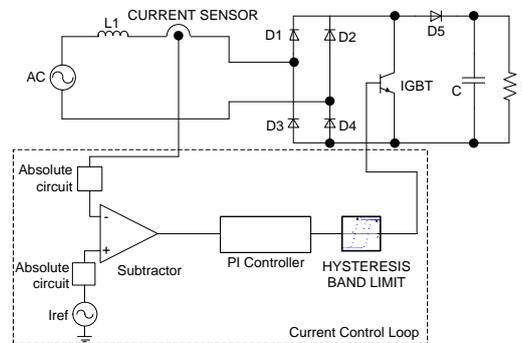


Figure 1. The proposed HCC in APF

3. COMPUTER SIMULATION MODEL

The circuit simulation of the proposed model began with the basic rectifier circuit without any filter. The rectifier is connected with power supply and RC load. The specification of the HCC designs is as tabulated in Table 1. Figure 2 shows the HCC model in the current loop circuit.

To verify the supply current waveform to follow the reference signal, the supply current is varied to be increase or to be reduce. The proposed configuration technique model is verified by using step response as shown in Figure 3. The use of step response in this technique is to verify that the input current will follow the shape and amplitude of desired reference signal. The modeling of step response controller is illustrated in Figure 4 by adding reference current and varying current as the input to the HCC.

Table 1. Circuit specifications

| Variable | Values |
|----------------------------|--------------|
| Supply voltage, V_s | 40Vrms |
| Boost inductor, L_s | 1mH |
| DC capacitor filter, C_o | 1000 μ F |
| Load resistor, R_L | 300 Ω |
| Frequency, f_s | 50kHz |
| Proportional Gain, K_p | 80 |
| Integral gain, K_i | 1000 |

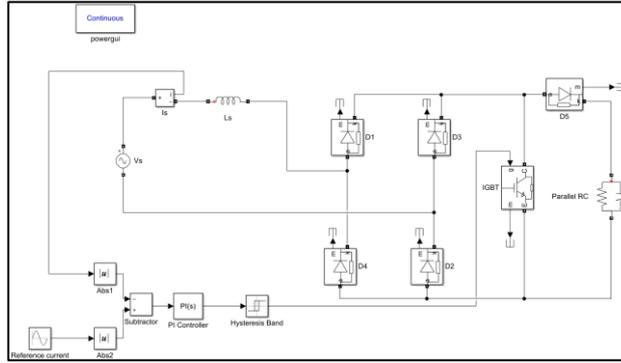


Figure 2. APF with HCC model

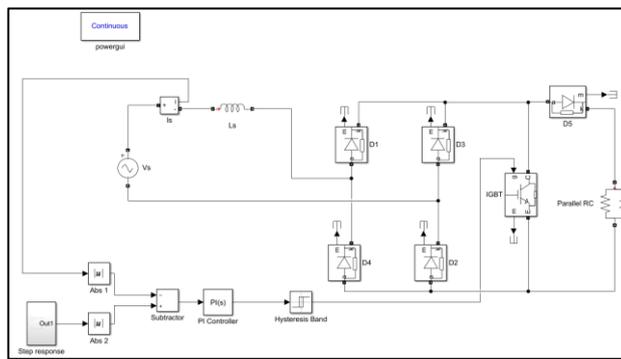


Figure 3. Step response model

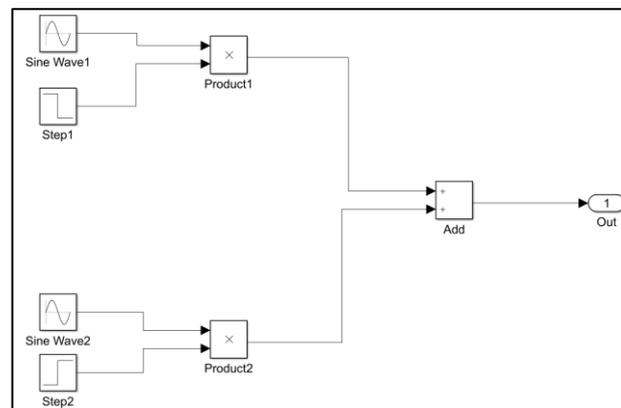


Figure 4. Step response sub-system

4. RESULTS AND DISCUSSION

Figure 5 shows the result of input current waveform of the converter without APF function. This state shows distortion in the supply current and consists of harmonics where the THD level are exceeding the IEEE 519 standard. With the proposed HCC in the APF function, the supply current waveform is in-phase with the supply voltage waveform shown in Figure 6. Figure 7 shows the supply current following the sinusoidal reference signal. The switching losses in the supply current are controlled by the band limit. The limit of the band affects the output of the waveform and the hysteresis band are kept within 5% from the reference current.

Table 2 shows the THD level of the input current waveform between the rectifier without filter and after applying the proposed APF with HCC. The results show that by applying HCC, the THD level can be reduce and comply with the IEEE 519 standard. Figures 8 and 9 show the step response signal representing

step increase and decrease of the reference signal to verify that the supply current waveform keep track the shape of the reference signal with a propoer designed of current control loop.

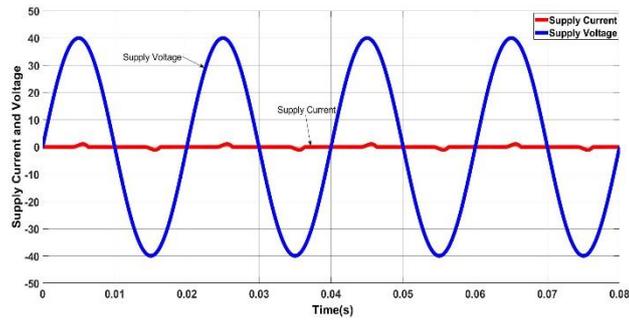


Figure 5. Non-sinusoidal input current

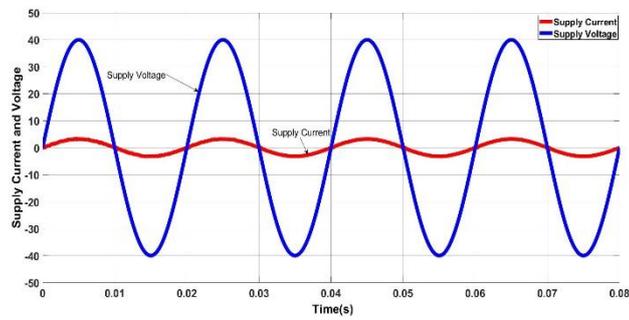


Figure 6. Sinusoidal input current in phase with the voltage waveforms

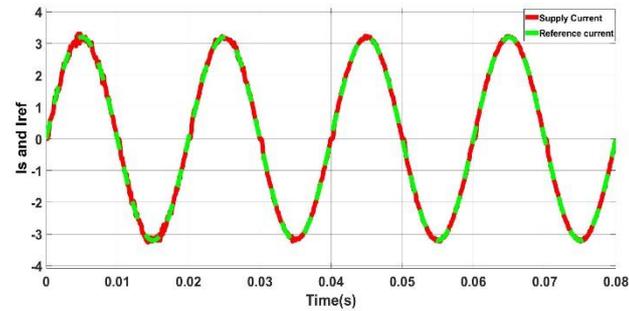


Figure 7. Input current keep track the reference signal by applying APF with HCC

Table 2. The comparison of THD level

| Parameters | THD level (%) |
|-------------|---------------|
| No filters | 152.36 |
| APF and HCC | 3.68 |

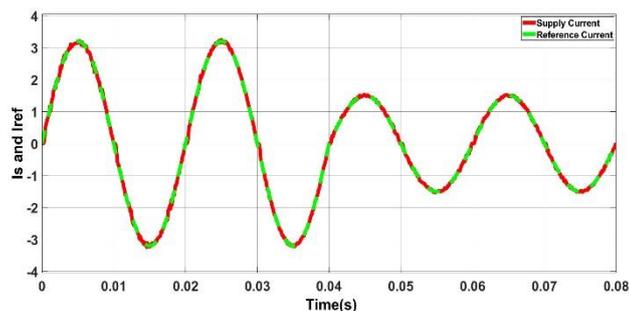


Figure 8. The step response signal with $I_{ref} = 3.2A$ at 0.04s and $I_{ref} = 1.5A$ at 0.08s

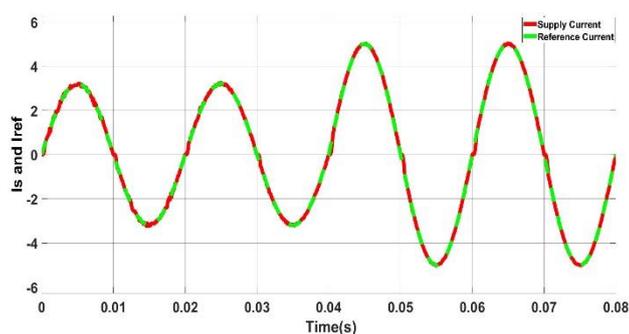


Figure 9. The step response signal with $I_{ref} = 3.2A$ at 0.04s and $I_{ref} = 5A$ at 0.08s

5. CONCLUSION

This paper has been presented the simulation results for the proposed APF with HCC method. The used of HCC can reduce the switching losses in sinusoidal supply current signal and reduce the THD level to comply with IEEE 519 standard, thus, solve the power quality problems. For future recommendations, it is highly recommended to apply the Hysteresis Current Control techniques in domestic applications and industrial sectors such as three-phase induction motors. The use of such method in three-phase induction motors can solve the distorted supply current waveform due to the non-linear torque, flux and current regulation.

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